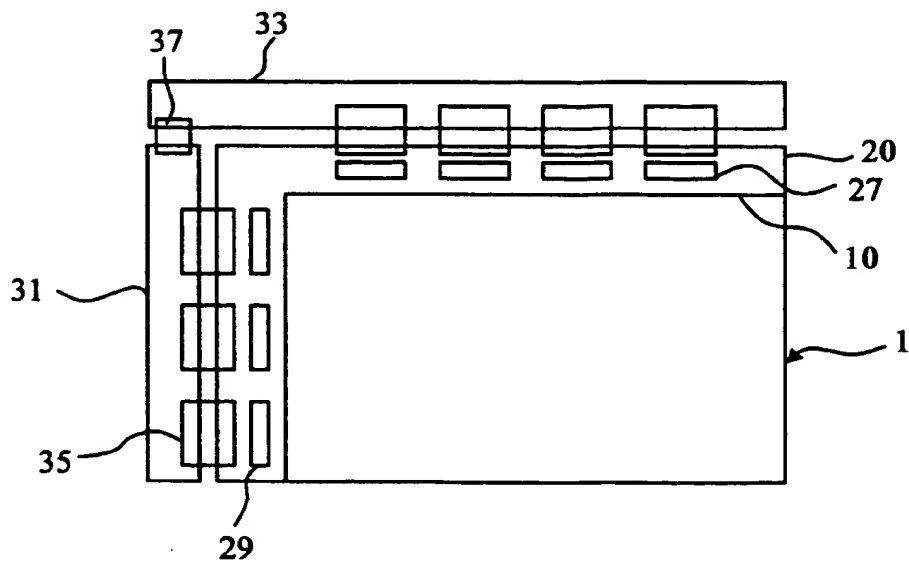
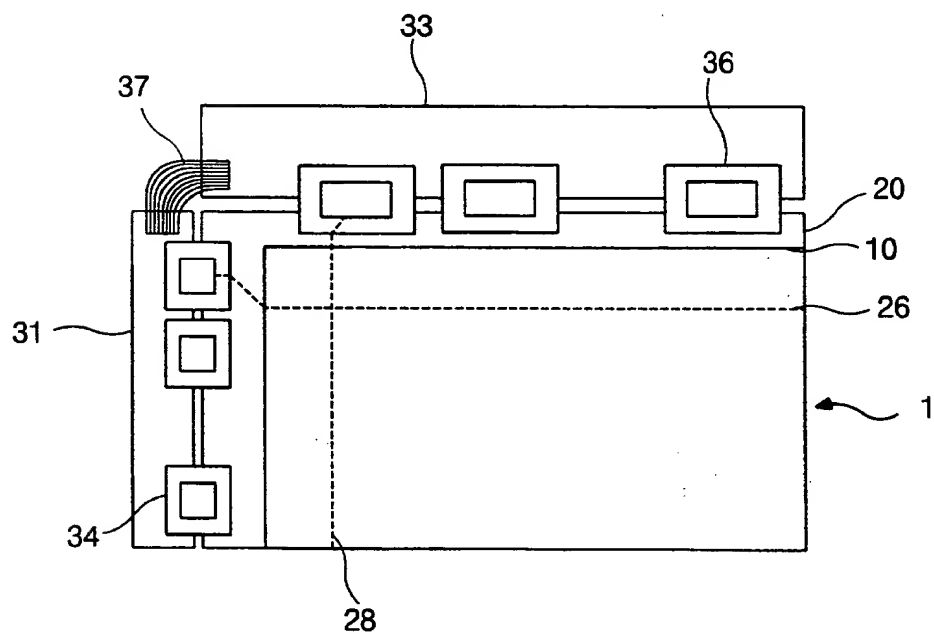


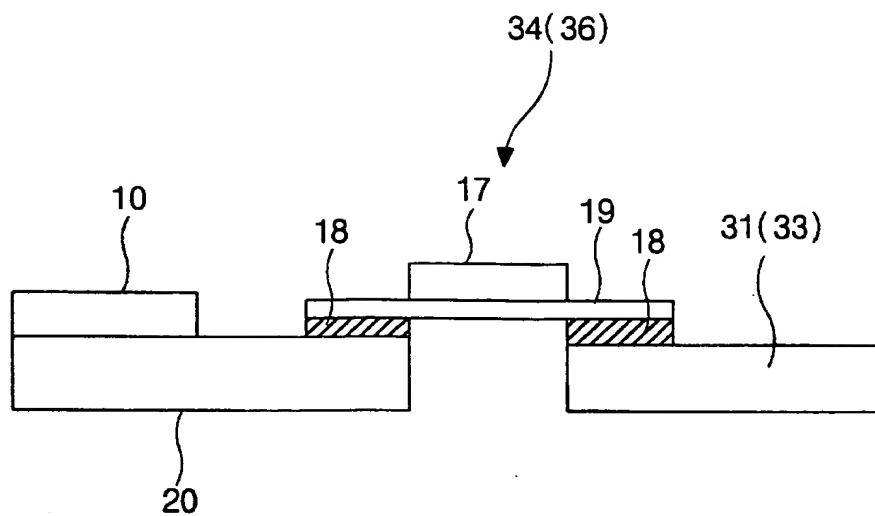
**FIG 1**



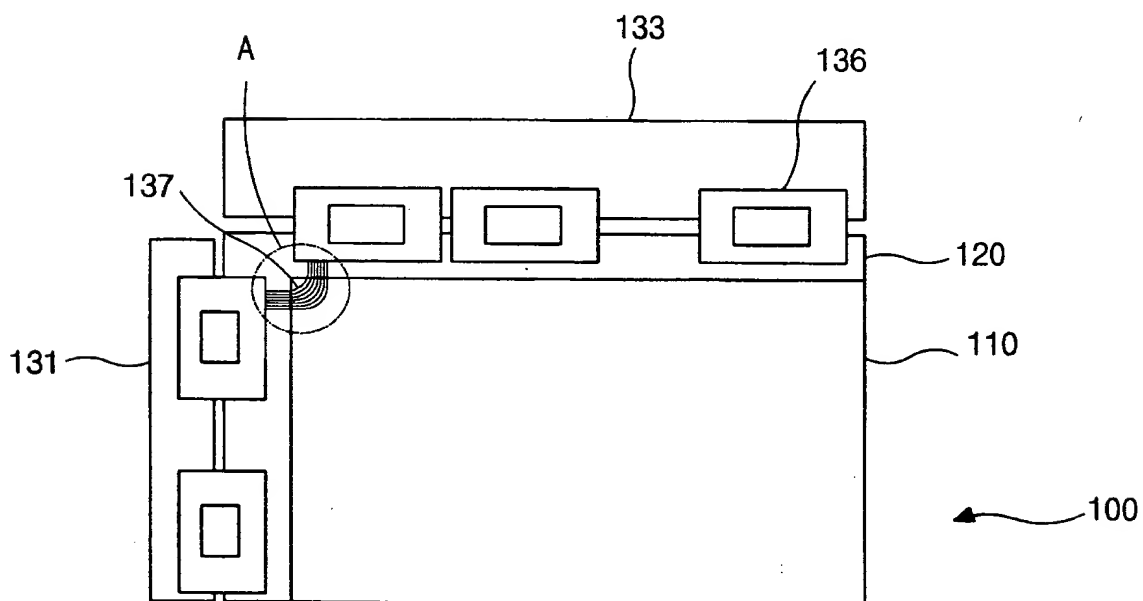
(related art)  
FIG 2



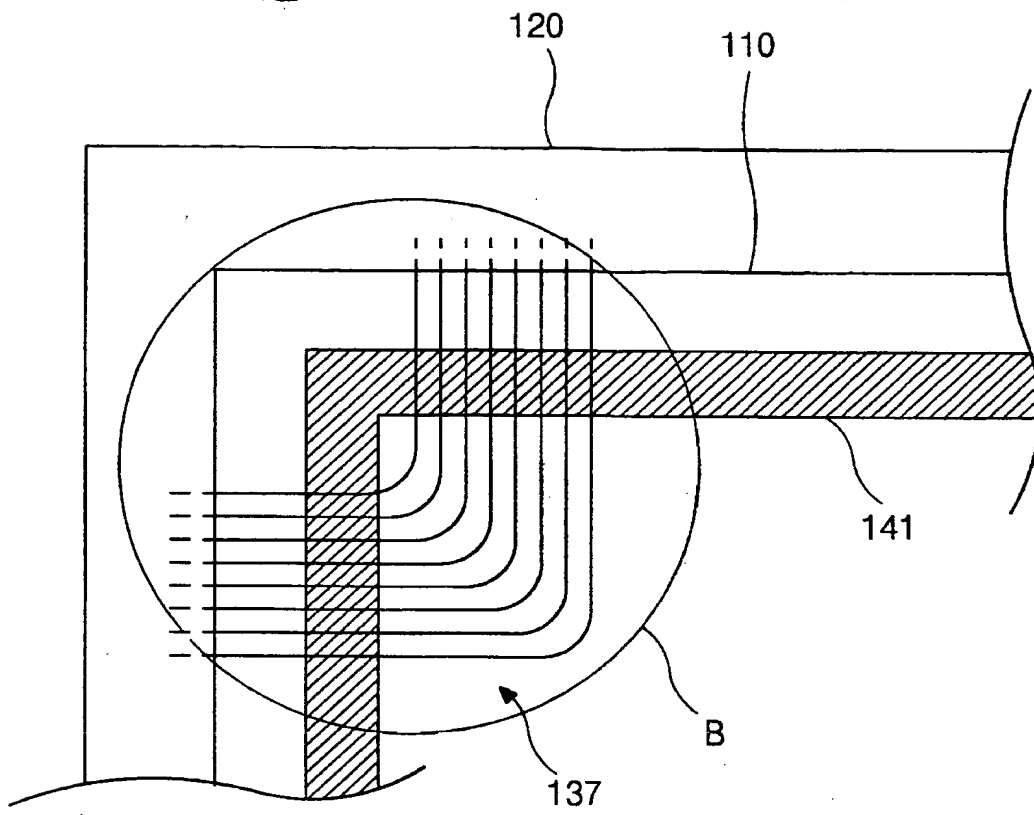
(related art)  
FIG 3



***(related art)***  
***FIG. 4***

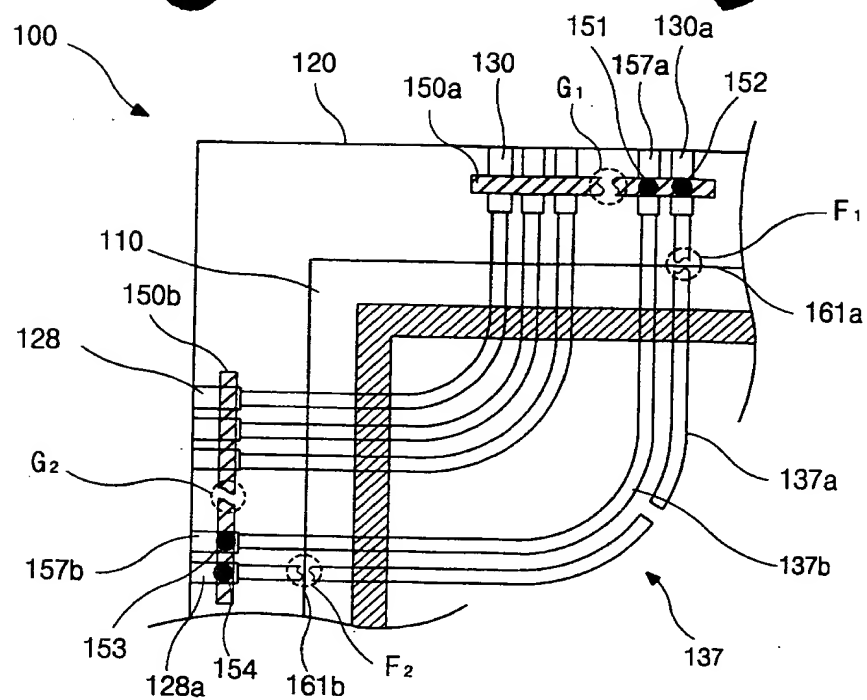


**FIG 5**

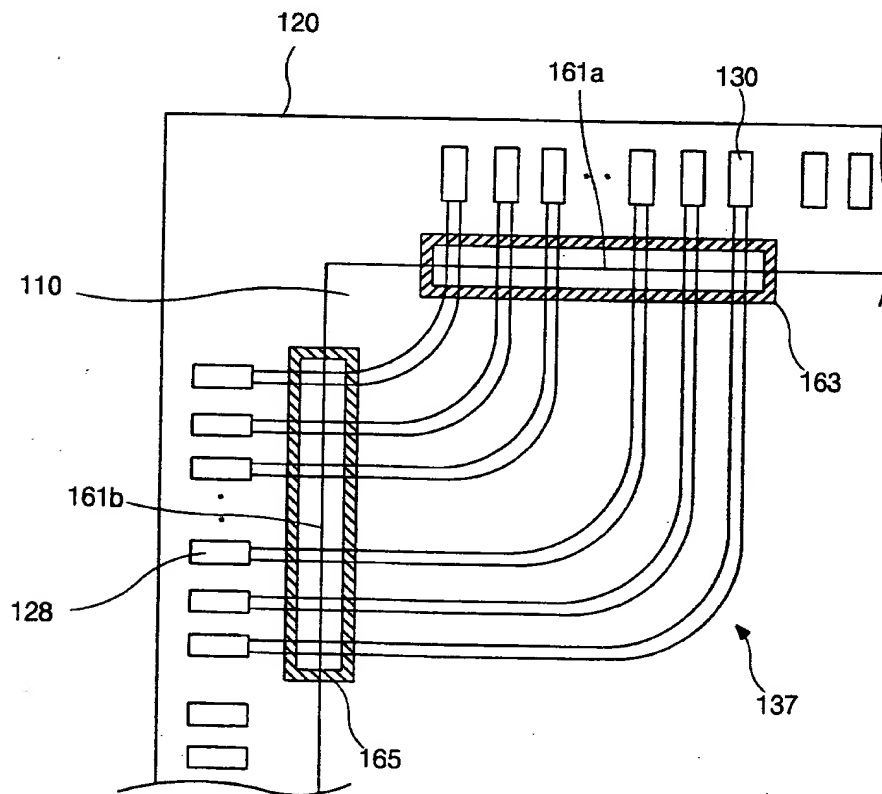


**FIG 6**



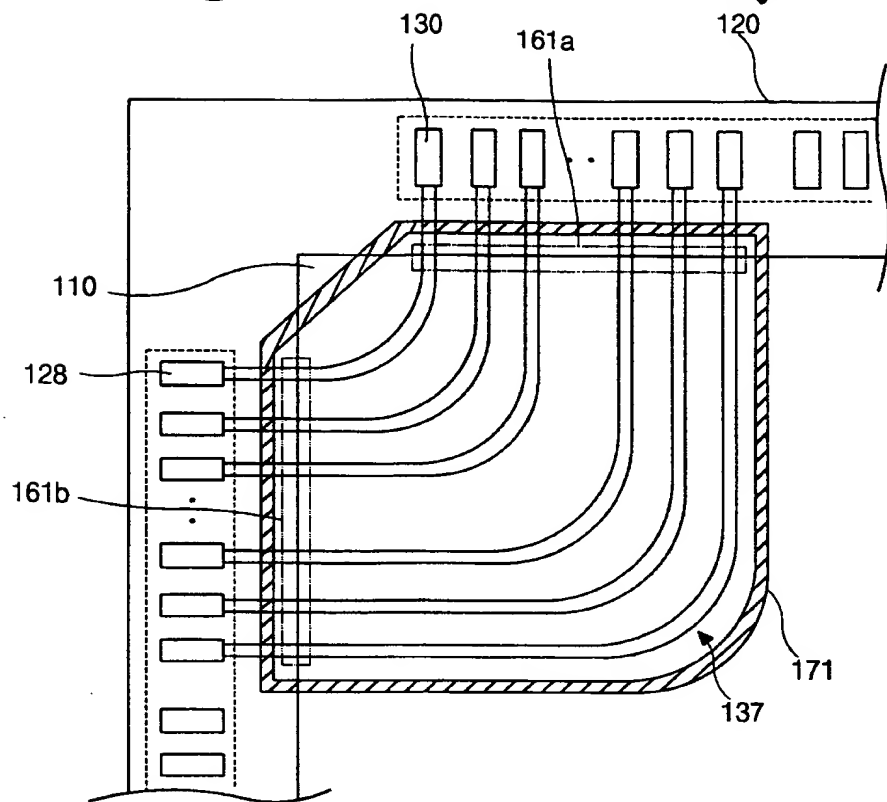


**FIG 9**



**FIG 10A**

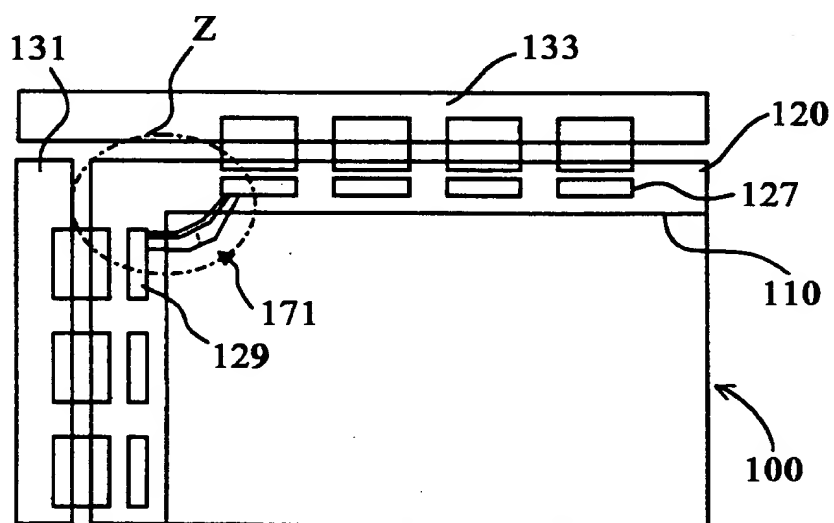
31



**FIG 11A**



**FIG. 11B**



**FIG 12**

FIG. 1 is a schematic diagram of a circuit board layout. It shows a grid of conductive traces. A horizontal trace 120 and a vertical trace 128 intersect. A curved trace 130 is connected to the horizontal trace at point X2 and to the vertical trace at point X1. Other traces are labeled 137, 137a, 161a, and 161b. Ellipses indicate additional traces and components.

**FIG 13B**



This cross-sectional view shows a substrate 120 with a thin layer 190 on top. A series of rectangular features 180b are formed on the surface, each containing a hatched region. A layer 137 is deposited over these features, with specific regions labeled 137a and 137c. A vertical gap or trench is shown between the features. The cross-section is bounded by dashed lines labeled XVI and XVI.

**FIG. 16A**

